

ONIX-FMC-LoopBack

Loopback for FMC HPC Connector

Product Overview

This document provides information regarding the ONIX-FMC-LOOPBACK board. The ONIX-FMC-LOOPBACK is VITA 57.1 compliant single-width HPC FMC, designed for the ONIX module, the ONIX-VU440-2, or various VITA 57.1 compliant carrier cards.

It provides looping back high-speed transmit-receive signals for various tests of the FMC HPC connector on the carrier boards.

Block Diagram

The ONIX-FMC-LOOPBACK board block diagram is shown in Figure 1.

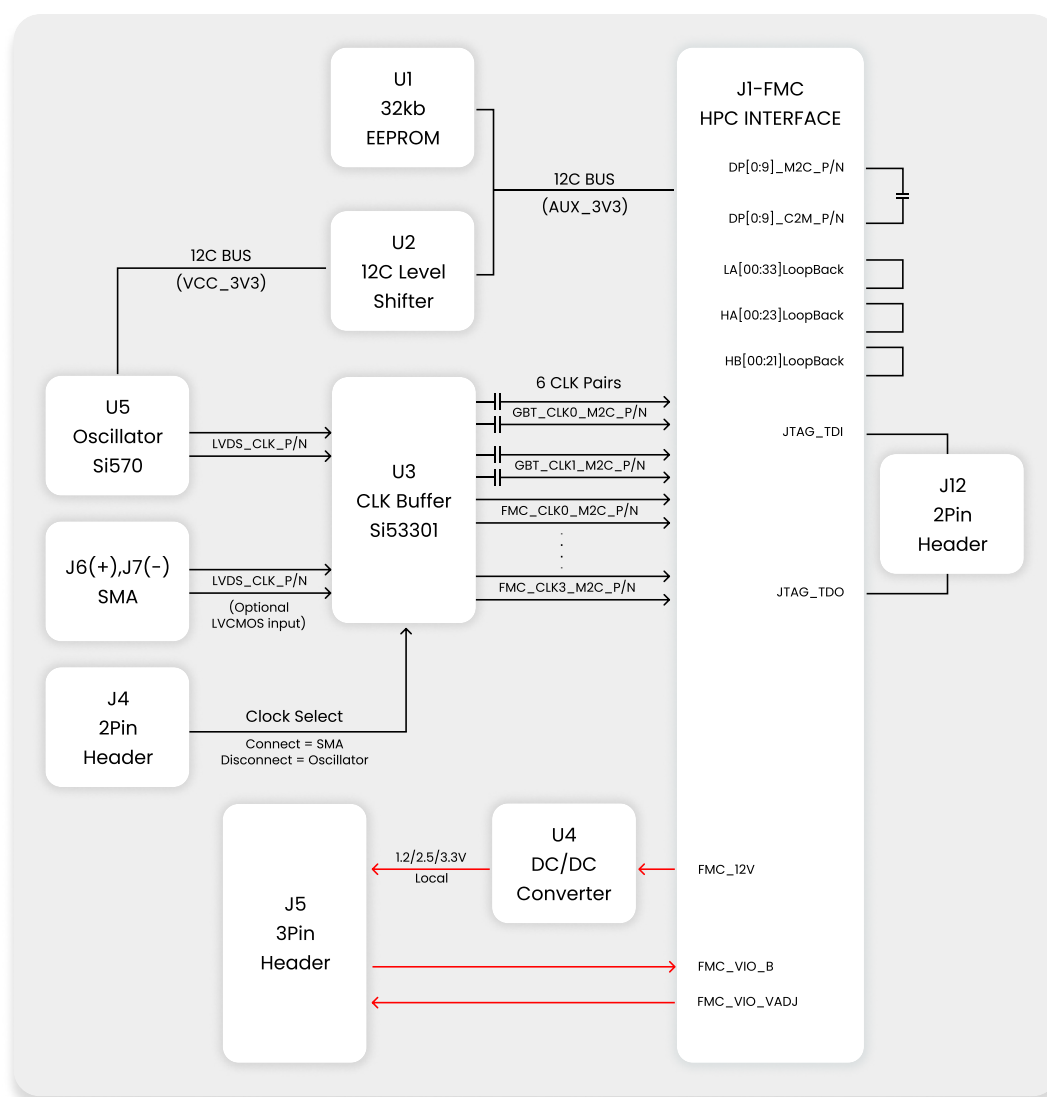


Figure 1 - ONIX-FMC-LOOPBACK Block Diagram

Main Features

- VITA 57.1 HPC FMC connector.
- On-board Loopback for LA[00:33], HA[00:23], HB[00:21] and DP[0:9] signals.
- 6 ref clocks for GBTCLK[1:0]_M2C_P/N and CLK[3:0]_M2C_P/N.
- Onboard DC/DC converter for VIO_B_M2C.
- 2 x SMA connectors for reference clock Input.
- 32Kb I2C EEPROM.

Ordering Information

Part Number	Description	Price
ONIX-FMC-LoopBack	Loopback for FMC HPC Connector	500\$

Related Products

Part Number	Description	Price
ONIX-VU440-2	ASIC Prototyping Board based on the AMD XCVU440-2 FPGA	30,000\$

Contact Us

5 Hatidhar St. Raanana 4366507, Israel // Tel: +972-9-9660967

Email: info@dgtronix-tech.com // Visit us at: <https://www.dgtronix-tech.com>